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Patent  
PD-201161  
Customer No. 020991

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Cocchi, Ronald P., :  
et al. :

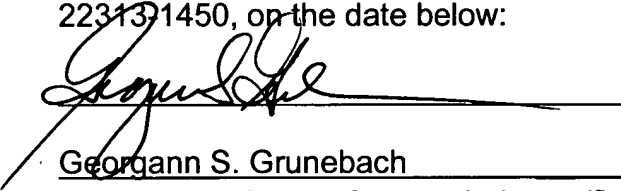
Serial No.: 10/085,860 :  
Filed: February 28, 2002 :

For :

ASYNCHRONOUS CONFIGURATION :

Group Art Unit: 2134 :  
Examiner: Morse, Gregory Allan :

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The references cited on the enclosed forms PTO/SB/08A and PTO/SB/08B may be material to the examination of the above-identified patent application and are, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR 1.56. In accordance with the recent Notice of the USPTO regarding the submission of Information Disclosure Statements without copies of the U.S. patents and published applications in patent applications filed after June 30, 2003, Applicant submits herewith copies of foreign patent documents and non-patent literature only, if applicable. It is respectfully requested that the references be considered and made of record in the application identified above.

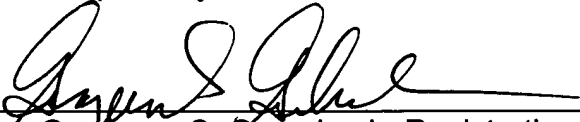
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Respectfully submitted,

By:

  
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|------------------------|----------------------|
| Application Number     | 10/085,860           |
| Filing Date            | February 28, 2002    |
| First Named Inventor   | Cocchi, Ronald P.    |
| Art Unit               | 2134                 |
| Examiner Name          | Morse, Gregory Allan |
| Attorney Docket Number | PD-201161            |

## U. S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

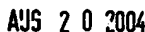
| Examiner Initials* | Cite No. <sup>1</sup> | Foreign Patent Document   | Publication Date<br>MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear | T |
|--------------------|-----------------------|---|--------------------------------|---|---|---|
|                    |                       | Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known) |                                |   |   |   |
|                    |                       | EP 0 851 358  |                                |   |   |   |
|                    |                       | EP 0 971 361  |                                |   |   |   |
|                    |                       | EP 0 984 403  |                                |   |   |   |
|                    |                       | EP 1 050 821  |                                |   |   |   |
|                    |                       | EP 1 085 516  |                                |   |   |   |
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| Filing Date            | February 28, 2002    |
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| Art Unit               | 2134                 |
| Examiner Name          | Morse, Gregory Allan |
| Attorney Docket Number | PD-201161            |

## U. S. PATENT DOCUMENTS

| Examiner Initials* | Cite No. <sup>1</sup> | Document Number                          | Publication Date<br>MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
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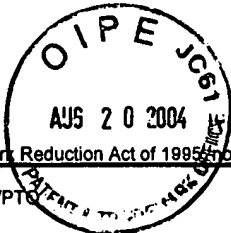
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|                    |                       | EP 1 074 906  |                                |   |   |                |
|                    |                       | WO 00 77717   |                                |   |   |                |
|                    |                       | WO 02 093332  |                                |   |   |                |
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| Examiner Name          | Morse, Gregory Allan |
| Attorney Docket Number | PD-201161            |

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**NON PATENT LITERATURE DOCUMENTS**

| Examiner<br>Initials* | Cite<br>No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|-----------------------|--------------------------|---|----------------|
|                       |                          | ARAKI, SHIGEO, "The Memory Stick", IEEE Micro, ISSN: 0272-1732, vol. 20, no. 4, July 2000 (2000-07)-August 2000 (2000-08), pages 40-46, XP002257044   |                |
|                       |                          | "Functional Model of a Conditional Access System", EBU Review-Technical, European Broadcasting Union. Brussels, Belgium, no. 266, 21 December 1995 (1995-12-21), pp. 64-77, XP000559450.  |                |
|                       |                          | KHOURI, OSAMA, et al., "Low Output Resistance Charge Pump for Flash Memory Programming", IEEE International Workshop on Memory Technology, Design and Testing (MTDT '01), San Jose, USA.  |                |
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